

FIG. 1

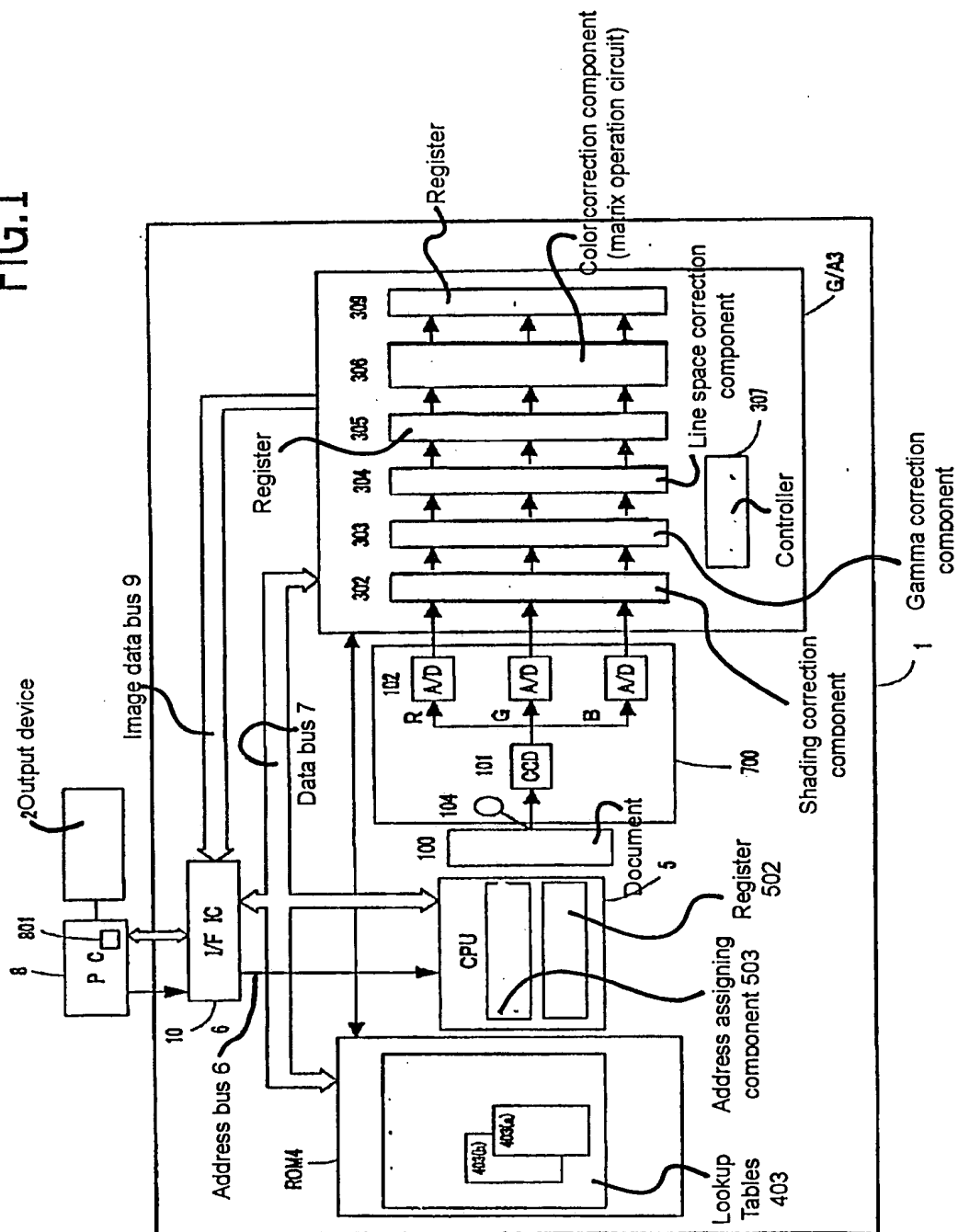


FIG.2

Lookup tables 403

Color lookup table 403(a)				Monotone lookup table 403(b)			
Matrix	Memory	Address		Matrix	Memory	Address	
A 1 $\begin{pmatrix} K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{pmatrix}$	R 1	K ₁₁	0H	Y 1	R 1	N ₁₁	0H
		K ₂₁	1H			N ₂₁	1H
		K ₃₁	2H			N ₃₁	2H
	G 1	K ₁₂	3H		G 1	N ₁₂	3H
		K ₂₂	4H			N ₂₂	4H
		K ₃₂	5H			N ₃₂	5H
	B 1	K ₁₃	6H		B 1	N ₁₃	6H
		K ₂₃	7H			N ₂₃	7H
		K ₃₃	8H			N ₃₃	8H
	A 2 $\begin{pmatrix} N_{11} & N_{12} & N_{13} \\ N_{21} & N_{22} & N_{23} \\ N_{31} & N_{32} & N_{33} \end{pmatrix}$	R 1	AH		Y 2	R 2	.
			BH				
			CH				
		G 1	DH				
			EH				
			FH				
	B 1	N ₁₃	10H				
		N ₂₃	.				
		N ₃₃	.				
	.	.	.				

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FIG.3

Register 502

Matrix	Memory	Address
$\begin{pmatrix} A1 \\ K_{11} & K_{12} & K_{13} \\ K_{21} & K_{22} & K_{23} \\ K_{31} & K_{32} & K_{33} \end{pmatrix}$ $\begin{pmatrix} A1 \\ K_{11} & K_{12} & K_{13} \\ K_{31} & K_{32} & K_{33} \\ K_{21} & K_{22} & K_{23} \end{pmatrix}$	R 1	K_{11} (K_{11}) 0 H
		K_{21} (K_{31}) 1 H
		K_{31} (K_{21}) 2 H
	G 1	K_{11} (K_{11}) 3 H
		K_{21} (K_{32}) 4 H
		K_{32} (K_{22}) 5 H
	B 1	K_{11} (K_{11}) 6 H
		K_{21} (K_{31}) 7 H
		K_{31} (K_{21}) 8 H

FIG. 4

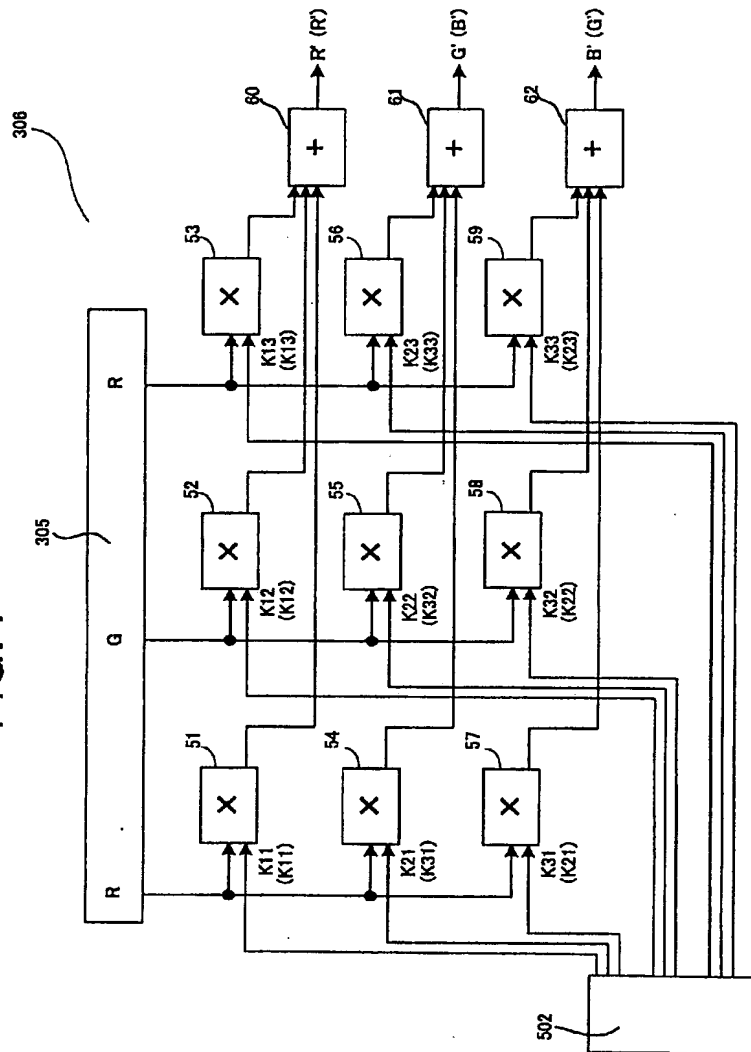


FIG.5

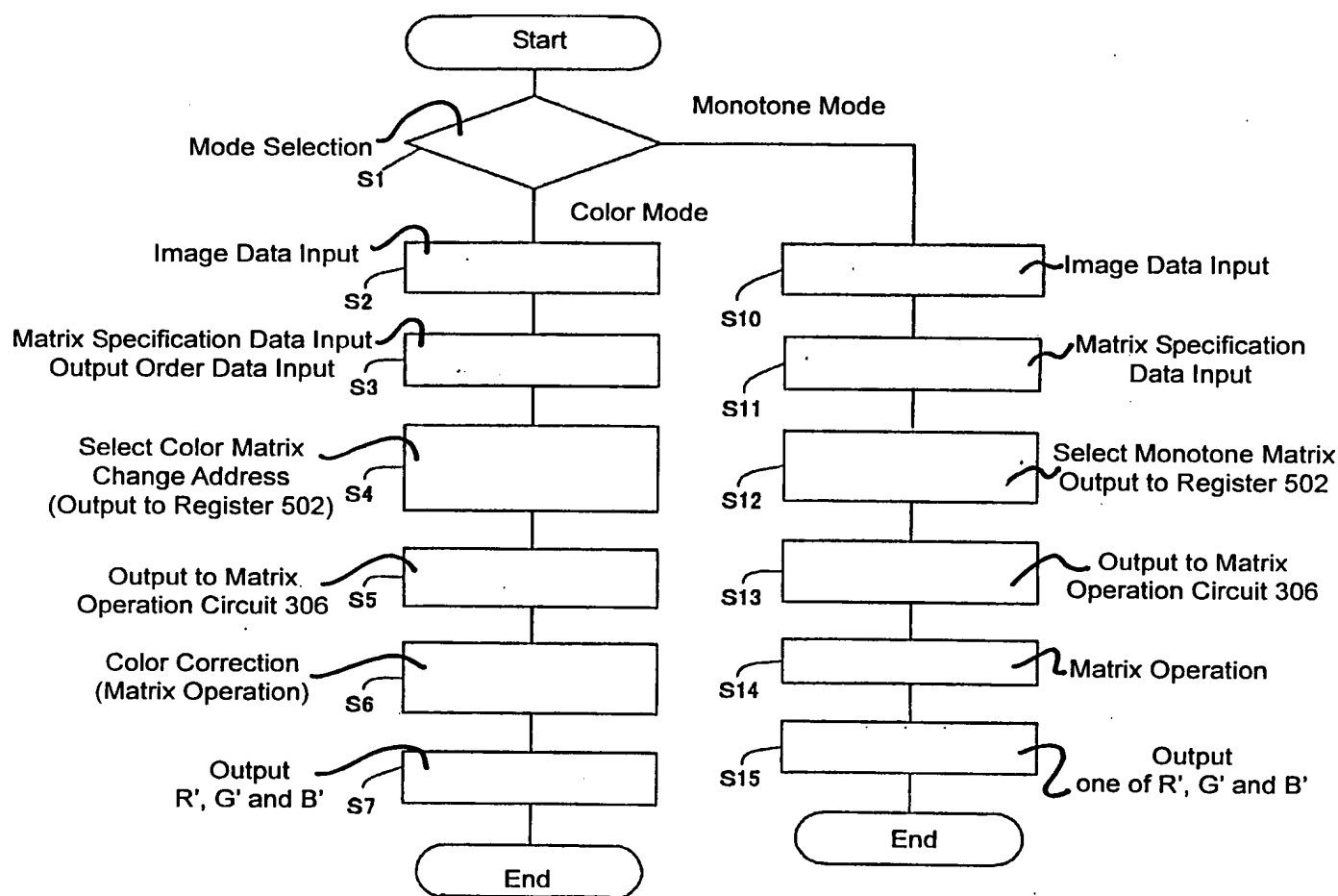


FIG.7

Lookup tables 403											
Color correction lookup table 403(a)				Output Order lookup table 403(b)				Monotone lookup table 403(c)			
Matrix	Memory	Address		Matrix	Memory	Address		Matrix	Memory	Address	
A 1	R 1	K ₁₁	0 H	V 1	R 1	1	0 H	Y 1	R 1	N ₁₁	0 H
		K ₂₁	1 H			0	1 H			N ₂₁	1 H
		K ₃₁	2 H			0	2 H			N ₃₁	2 H
	G 1	K ₁₂	3 H		G 1	0	3 H		G 1	N ₁₂	3 H
		K ₂₂	4 H			1	4 H			N ₂₂	4 H
		K ₃₂	5 H			0	5 H			N ₃₂	5 H
	B 1	K ₁₃	6 H		B 1	0	6 H		B 1	N ₁₃	6 H
		K ₂₃	7 H			1	7 H			N ₂₃	7 H
		K ₃₃	8 H			0	8 H			N ₃₃	8 H
A 2	R 2	.	.	V 2	R 2	1	A H	Y 2	R 2	.	.
		.	.			0	B H			.	.
	
	

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$$V_4 \begin{bmatrix} G \\ B \\ R \end{bmatrix} = \begin{bmatrix} 0 & 1 & 0 \\ 0 & 0 & 1 \\ 1 & 0 & 0 \end{bmatrix} \begin{bmatrix} R \\ G \\ B \end{bmatrix}$$

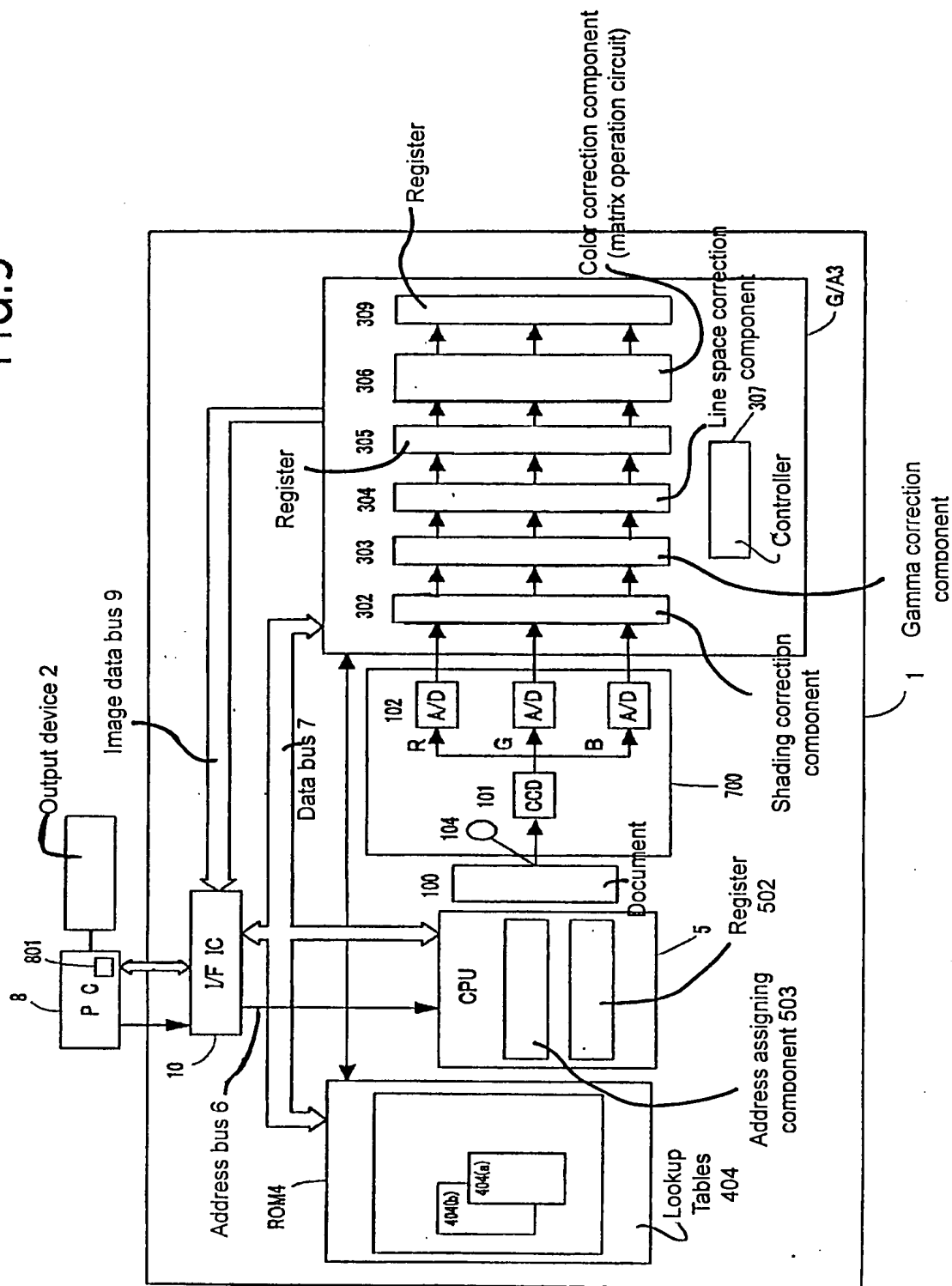
FIG. 8E

$$\begin{pmatrix} B \\ G \\ R \end{pmatrix} = \overset{V5}{\begin{pmatrix} 0 & 0 & 1 \\ 0 & 1 & 0 \\ 1 & 0 & 0 \end{pmatrix}} \begin{pmatrix} R \\ G \\ B \end{pmatrix}$$

FIG. 8F

$$\begin{pmatrix} B \\ R \\ G \end{pmatrix} = \overset{V6}{\begin{pmatrix} 0 & 0 & 1 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \end{pmatrix}} \begin{pmatrix} R \\ G \\ B \end{pmatrix}$$

FIG. 9



PRIOR ART FIG.11

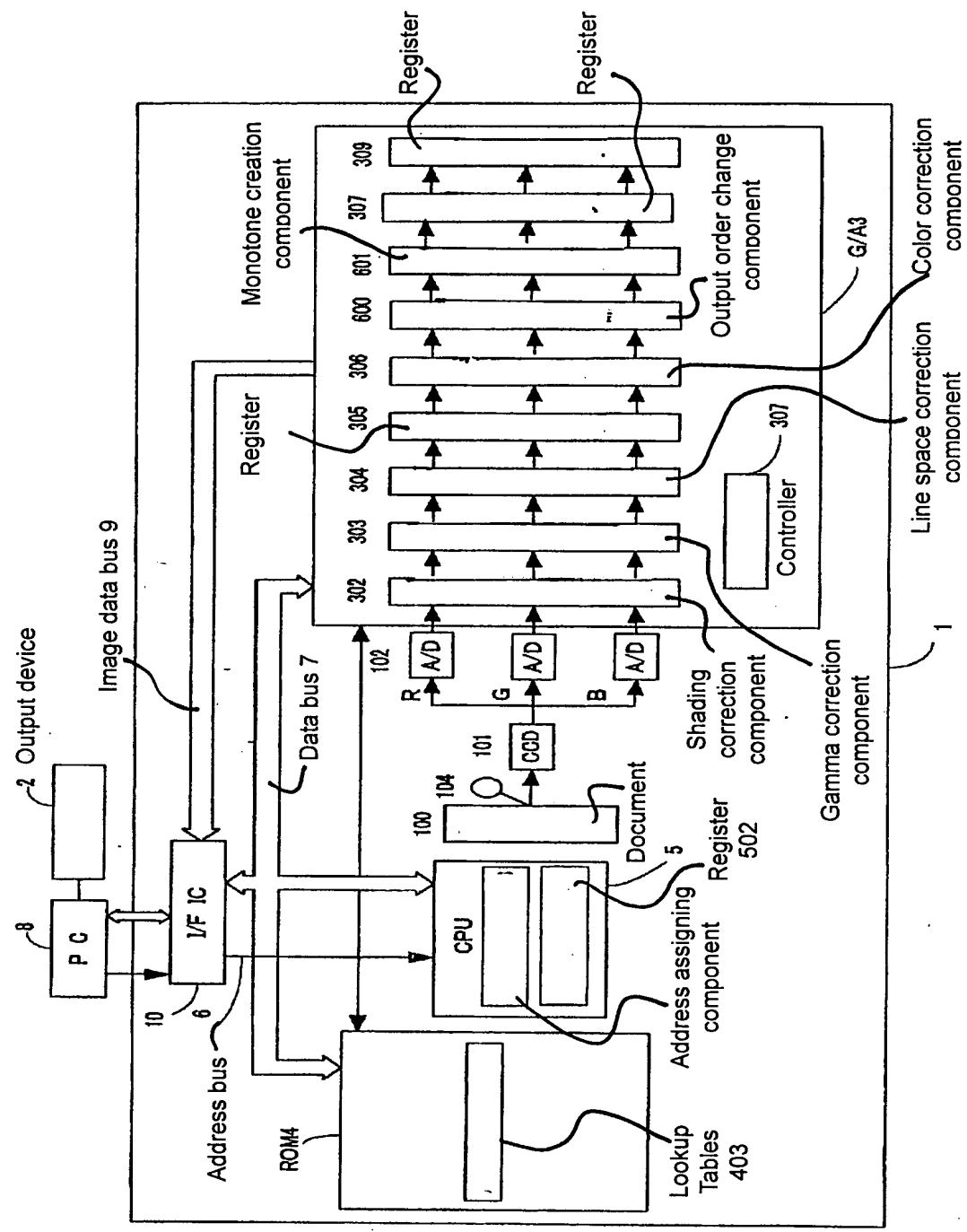


FIG.10

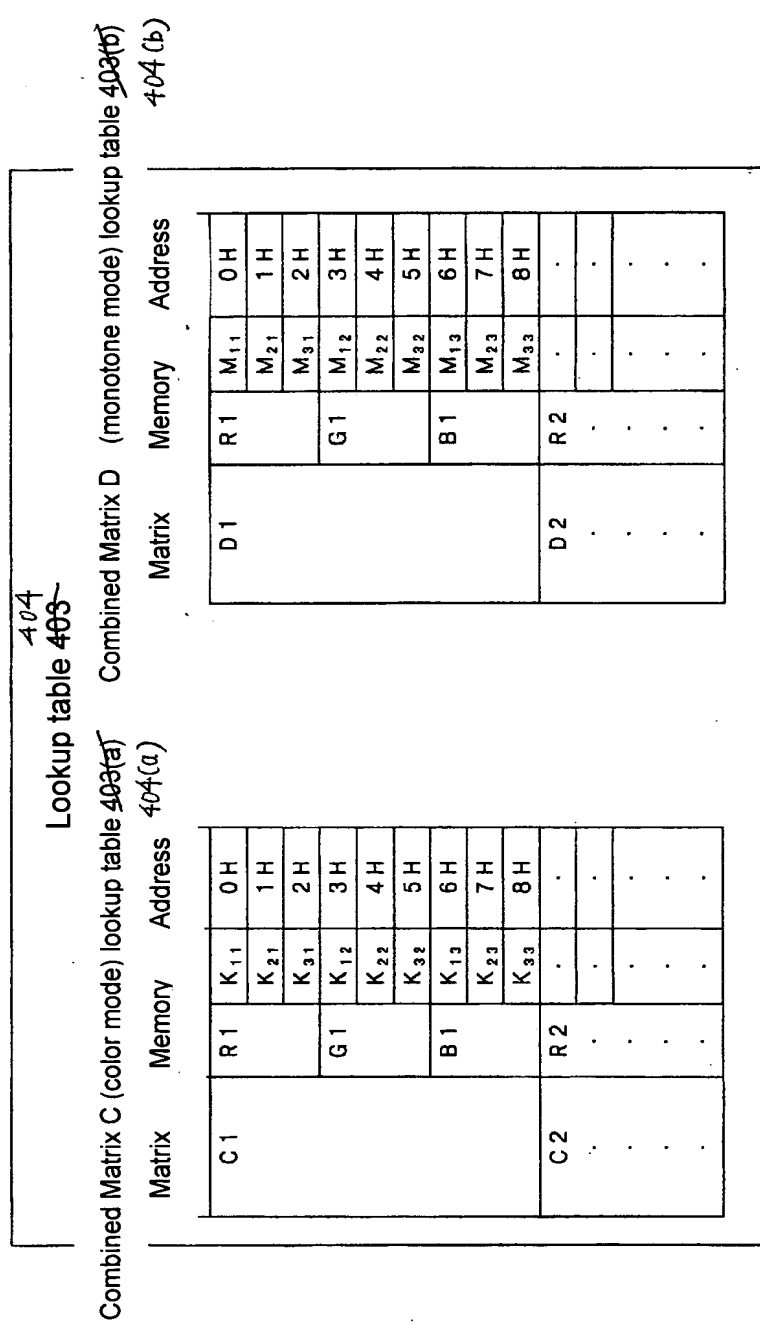


FIG.10

